

**REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2183**

PATENT
5500-88700/TT5374

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:	10/726,902	§	Examiner:	Fennema, Robert E.
Filed:	December 3, 2003	§	Group/Art Unit:	2183
Inventors:		§	Atty. Dkt. No.:	5500-88700
	Mitchell Alsup	§		
	Gregory William Smaus	§		
		§		
		§		
Title:	Transitioning From Instruction Cache to Trace Cache on Label Boundaries	§		
		§		
		§		
		§		
		§		
		§		
		§		
		§		
		§		

RESPONSE TO FINAL ACTION OF JULY 18, 2007

MAIL STOP AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Final Action of July 18, 2007, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.